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Election of Claims

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

5 **Patent Application**

Inventors: Master, Paul L. et al.

Examiner: Kim, Kenneth S.

Serial No.: 10/022,776

Group Art Unit: 2111

Docket No.: 021202-001000US

Filed: 12/13/2001

10 **Entitled:** Computer Processor Architecture Selectively Using Finite-State Machine for Control Code Execution

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**CLAIM ELECTION UNDER 37 CFR 1.143
IN RESPONSE TO RESTRICTION REQUIREMENT**

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Mail Stop Amendment
Honorable Commissioner of
Patents and Trademarks
Alexandria, VA 22313-1450

Sir:

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In response to the restriction requirement in the Office Action mailed October 17, 2006 (Confirmation No. 6960), Applicants hereby respectfully submit the following Election of Claims under 37 CFR 1.143, electing Group I without traverse.

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In the event of non-payment or improper payment of any required fee, the Commissioner is authorized to charge or to credit Deposit Account No. 501,262 as may be required to correct the error.